A Fully Printed Flexible MoS$_2$ Memristive Artificial Synapse with Femtojoule Switching Energy

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Realization of memristors capable of storing and processing data on flexible substrates is a key enabling technology toward “system-on-plastics”. Recent advancements in printing techniques show enormous potential to overcome the major challenges of the current manufacturing processes that require high temperature and planar topography, which may radically change the system integration approach on flexible substrates. However, fully printed memristors are yet to be successfully demonstrated due to the lack of a robust printable switching medium and a reliable printing process. An aerosol-jet-printed Ag/MoS$_2$/Ag memristor is realized in a cross-bar structure by developing a scalable and low temperature printing technique utilizing a functional molybdenum disulfide (MoS$_2$) ink platform. The fully printed devices exhibit an ultra-low switching voltage (0.18 V), a high switching ratio ($10^7$), a wide range of tuneable resistance states (10–10$^10$ Ω) for multi-bit data storage, and a low standby power consumption of 1 fW and a switching energy of 4.5 fJ per transition set. Moreover, the MoS$_2$ memristor exhibits both volatile and non-volatile resistive switching behavior by controlling the current compliance levels, which efficiently mimic the short-term and long-term plasticity of biological synapses, demonstrating its potential to enable energy-efficient artificial neuromorphic computing.

1. Introduction

Neuromorphic computing shows great potential beyond the traditional von Neumann architecture to enable a myriad of innovative applications including personalized feedback therapy, fast speech, and visual recognition.[1–5] In particular in the non-conventional space of smart applications requiring conformal attachment on non-flat surfaces such as on-body wearables, the notion of system on plastics (SOP) incorporating neuromorphic computing provides a potential solution.[1,2] To build such a flexible neuromorphic system, the fabrication of memristors equipped with synaptic functions is a key step to forming the artificial neural network.[3–21] However, current memristor manufacturing technologies such as chemical vapor deposition (CVD),[7,8,11–13] spin-coating,[14,15] or entire transfer[16] impose enormous challenges on flexible substrates as they suffer from high temperature, low yield and complex sacrificial layer removal. Efforts in finding low temperature fabrication technique and robust resistive switching (RS) material are essential to equip the SOP with data storage and processing capability demanded by target applications.

The printing technique—a forefront 3D monolithic integration approach—is suitable for high-volume, low-temperature manufacturing on non-conformal surfaces.[22–28] The printing technique is shown to offer more freedom in the design of and intelligent applications including personalized feedback therapy, fast speech, and visual recognition.[1–5] In particular in the non-conventional space of smart applications requiring conformal attachment on non-flat surfaces such as on-body wearables, the notion of system on plastics (SOP) incorporating neuromorphic computing provides a potential solution.[1,2] To build such a flexible neuromorphic system, the fabrication of memristors equipped with synaptic functions is a key step to forming the artificial neural network.[3–21] However, current memristor manufacturing technologies such as chemical vapor deposition (CVD),[7,8,11–13] spin-coating,[14,15] or entire transfer[16] impose enormous challenges on flexible substrates as they suffer from high temperature, low yield and complex sacrificial layer removal. Efforts in finding low temperature fabrication technique and robust resistive switching (RS) material are essential to equip the SOP with data storage and processing capability demanded by target applications.

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device geometry and the fabrication of heterostructures. It shows advantages beyond simple solution processed techniques such as spin coating, particularly for large scale fabrication.[22–28] However, studies on printable RS media are lacking. Among the conventional RS media, including transition metal oxides (TMOs, e.g., HfO2, Al2O3, TiO2),[14,9,10,16] and two-dimensional materials (2DMs),[7,8,11–14,17–21] the latter shows great potential to realize printed memristors with their solution processability and inherent flexibility. Most printed 2DM demonstration shows robust flexibility.[22–28] However, the presence of defects density/vacancies in solution exfoliated 2DM crystals is found to compromise the electrical performance.[21,22] On the other hand, the defects such as sulfur S vacancies in MoS2 introduced during ultrasonic assisted liquid phase exfoliation (UALPE), play a key role in realizing successful RS by allowing metal ion penetration to form the conductive filaments,[7,9] analogous to those observed in biological synapses such as Ca2+ influx to initialize plasticity.[9,30,31] In recent years, molybdenum disulfide (MoS2) has emerged as a high-performance RS material[8,11–14,17–21] with high switching ratio of 10^7. In addition, an ultra-low switching voltage is ultra low at 0.18 V (≈300 °C), it is not suitable for reliable low temperature, uniform deposition of the MoS2 flakes onto low energy PI substrate. We therefore use a solvent exchange process (high speed centrifugation, followed by redispersion of the supernatant by mild sonication) to formulate a stable ink in IPA and 2-butanol mixture. Previously reported binder-based ink strategy may require an additional processing step (e.g., photonic[32] or annealing[26]) to remove the polymeric binders after printing or water-based inks that may also require high temperature (>300 °C) post-print annealing to remove residual moisture.[9] In contrast, our formulated MoS2 ink dries rapidly at room temperature (~150 ms for 40–50 µl individual droplets) without requiring post-deposition processing and enables a uniform deposition through Marangoni enhanced spreading due to the composition of the ink carrier.[27] Figure 1c shows an example of aerosol-jet printed MoS2 ink on different layers. The in-house formulated MoS2 ink composed of UALPE of MoS2 crystals in a binary solvent carrier of isopropyl alcohol (IPA) and 2-butanol, which allows stable jetting and rapid ink drying. The RS mechanism is modeled to be driven by Ag metallic ion diffusion along the carrier of isopropyl alcohol (IPA) and 2-butanol, which allows stable jetting and rapid ink drying. The dispersion is then centrifuged to remove the unexfoliated flakes from the bulk crystals[33] (Figure 1e). The scanning electron microscopy (SEM) image in Figure 1f shows that the lateral size of the exfoliated MoS2 flakes is ≈5–10 µm. The ultrasonic atomization process of aerosol-jet printer further breaks down the MoS2 flakes into smaller flakes of ≈100–300 nm lateral dimensions. The AFM image in Figure 1g indicates that 10 printing passes ensure sufficient overlap between the deposited flakes to prevent the formation of pin-holes that could result in a short-circuit between the electrodes.

2. Results and Discussion

2.1. Aerosol-Jet Printing and MoS2 Ink Formulation

Figure 1a shows the working principles of aerosol-jet printing, which utilizes ultrasonic atomization to generate active ink droplets that are then carried by N2 gas onto the substrate surface during printing, enabling maskless patterning of materials with fine features. Details of the printing parameters are described in Section 4. Our in-house formulated binder-free MoS2 ink is composed of ultrasonic assisted liquid phase exfoliated (UALPE) MoS2 in a binary solvent carrier of 9:1 vol% IPA and 2-butanol. The UALPE employs pure shear forces to allow exfoliation of MoS2 flakes from the bulk in large quantities at a low setup and production cost.[28] Figure 1b shows a photograph of the formulated MoS2 ink, with a concentration of 0.5 mg mL^{-1}. To formulate the ink, a dispersion of MoS2 flakes is first prepared by UALPE of MoS2 crystals in N-methyl-2-pyrrolidone (NMP). The dispersion is then centrifuged to remove the unexfoliated crystals. Considering the high boiling point of NMP (~204 °C), it is not suitable for reliable low temperature, uniform deposition of the MoS2 flakes onto low energy PI substrate. We therefore use a solvent exchange process (high speed centrifugation, followed by redispersion of the supernatant by mild sonication) to formulate a stable ink in IPA and 2-butanol mixture. Previously reported binder-based ink strategy may require an additional processing step (e.g., photonic[32] or >200 °C annealing[26]) to remove the polymeric binders after printing or water-based inks that may also require high temperature (>300 °C) post-print annealing to remove residual moisture.[9] In contrast, our formulated MoS2 ink dries rapidly at room temperature (~150 ms for 40–50 µl individual droplets) without requiring post-deposition processing and enables a uniform deposition through Marangoni enhanced spreading due to the composition of the ink carrier.[27] Figure 1c shows an example of aerosol-jet printed MoS2 ink on different layers. The in-house formulated MoS2 ink composed of UALPE of MoS2 crystals in a binary solvent carrier of isopropyl alcohol (IPA) and 2-butanol, which allows stable jetting and rapid ink drying. The RS mechanism is modeled to be driven by Ag metallic ion diffusion along the carrier of isopropyl alcohol (IPA) and 2-butanol, which allows stable jetting and rapid ink drying. The dispersion is then centrifuged to remove the unexfoliated flakes from the bulk crystals[33] (Figure 1e). The scanning electron microscopy (SEM) image in Figure 1f shows that the lateral size of the exfoliated MoS2 flakes is ≈5–10 µm. The ultrasonic atomization process of aerosol-jet printer further breaks down the MoS2 flakes into smaller flakes of ≈100–300 nm lateral dimensions. The AFM image in Figure 1g indicates that 10 printing passes ensure sufficient overlap between the deposited flakes to prevent the formation of pin-holes that could result in a short-circuit between the electrodes.

2.2. Fully Printed MoS2 Memristor Architecture

Figure 2a illustrates the schematic of a two-terminal vertical crossbar structure and the photograph of a fully printed...
$4 \times 4$ crossbar MoS$_2$ memristor array on PI. The MoS$_2$ is printed as the resistive switching medium at the intersections between orthogonal rows and columns of silver (Ag) electrodes, in which they serve as the “bit” lines and “word” lines, respectively. It is worth noting that no complicated substrate treatment such as high temperature curing is required, and all printing processes are carried out in ambient condition. The cross-sectional high-resolution transmission electron microscopy (HR-TEM) image in the left panel of Figure 2b shows the printed memristor architecture in which the MoS$_2$ is sandwiched between the top and bottom Ag electrodes. The MoS$_2$ crystal structure is clearly identifiable in the zoomed-in image of the MoS$_2$ (the right panel of Figure 2b) wherein the structural stability of the layered materials could provide improvements in the robustness of the printed device. The image shows the existence of grain boundaries, which implies the polycrystalline nature of the printed MoS$_2$ flakes. The energy-dispersive X-ray spectrometry (EDX) elemental map in Figure 2c further confirms the presence and uniform distribution of Ag, Mo, and S elements. Notably, no oxide signal is detected inside the printed switching layers, which indicates that both the ink formulation and printing are oxidation-free processes. The surface properties of the printed MoS$_2$ thin films are investigated by X-ray photoelectron spectroscopy (XPS) as shown in Figure 2d. The binding energy of the most intense Mo 3d and S 2p spin–orbit split doublet can be assigned to 228.74 eV (Mo$^{4+}$ 3d$_{5/2}$) and 161.53 eV (S$^{2-}$ 2p$_{3/2}$), consistent with previously reported semiconducting MoS$_2$.\textsuperscript{[34]} The atomic composition ratio for Mo and S is found to be 1:1.71, which implies the existence of sulfur vacancies. Details of the XPS analysis is described in Section 4.

2.3. Volatile and Non-Volatile Resistive Switching

To characterize the switching performance, we collect current–voltage ($I$–$V$) switching curves measured at CC levels of 100 nA, 1 µA, and 10 µA, as shown in Figure 3a. All the $I$–$V$ curves
exhibit threshold-type volatile RS behavior, in which the memristor abruptly switches to a low resistance state (LRS) beyond a threshold voltage and then shows resistance recovery when the bias is removed, regardless of the polarity of the sweep voltages. A sharp turn-on slope of $\approx 5 \text{ mV dec}^{-1}$ is observed wherein the super nonlinearity implies its versatility to be used as a two-terminal selector in a 3D stack with memristor to suppress sneak current.\[35]\] Subsequently, 100 consecutive $I-V$ sweeps are performed which indicates the reproducibility of RS and a clear memory window, as shown in Figure 3b. The first sweep highlighted as red indicates the forming-free process. In TMO memristors, a forming-free process is typically required by applying a large voltage to initiate a soft dielectric breakdown and incorporate mobile ions into the insulating film to form a transport channel. Benefiting from the printed defective structure, the first forming voltage is comparable with the subsequent set voltage, which is desirable for practical applications. Interestingly, when a high CC of beyond 100 $\mu$A is applied, the switching characteristic toggles to a non-volatile behavior, as shown in Figure 3c. In this case, after setting the memristor in LRS with an ultra-low switching voltage of 0.18 V (at CC of 200 $\mu$A), a voltage bias with opposite polarity is required to reset the memristor to high resistance state (HRS). A high switching ratio of $10^7$ is achieved at a small reading voltage of 10 mV, the highest reported in flexible memristors so far.\[13,16,36-43\] It is noted that a larger CC during the set process might produce a larger and more stable conductive filament, thus a larger reset voltage and reset current is required. Even with the removal of the stress bias, the retention measurement shows that the LRS is retained for $>11$ h (Figure 3d). As a result of the low off current of $=0.1$ pA at a reading voltage of 10 mV as shown in Figure 3d, the standby power is derived as $P_{\text{standby}} = I_{\text{HRS}} \times V_{\text{read}} = 1$ fW. This effectively suppresses the sneak current and cells disturbance issues. Moreover, multiple resistance states with a wide tuneability range within 10–10$^{10}$ $\Omega$ are also recorded after setting the device to different CC, as shown in Figure 3e, showing great potential for enabling multi-level data storage and multi-state neuromorphic computing.\[10\] Bending tests are also performed to evaluate the mechanical stability of our printed MoS$_2$ memristor. Despite 1000 bending cycles with testing under different bending radii (down to 5 cm, Section S2, Supporting Information), no degradation in the switching ratio is observed, underscoring the mechanical robustness of the fully printed MoS$_2$ memristor device.

Figure 3f benchmarks the switching voltage and switching ratio of the fully printed MoS$_2$ memristor with the state-of-the-art flexible memristors. The printed memristor shows superior performance over its flexible memristor counterparts with a set voltage above 1 V\[13,16,36,37,39-43\] and deficient switching ratio of less than $10^3$.\[16,39-43\] Apparently, the 2DMs\[13,14,36-38\] (Region I) demonstrated competitive advantages over the conventional brittle transition metal oxide (TMO)\[16,39-43\] (e.g., NiO$_x$, TiO$_2$, Region II) and the organic materials (Region III)\[42,43\] due to their inherent flexibility and superior carrier transport properties. In addition, Figure 3f shows that memristors in a cross-bar structure fabricated by transfer method are usually suffering from unsatisfactory performance as compared with single device realized by mechanical exfoliation or spin coating.
probably due to the sneak current. The precisely controlled patterning of MoS$_2$ by printing effectively prevents the cell-to-cell interference and achieves excellent performance in a cross-bar structure. Thus, the aerosol-jet printing technique has significant merits for flexible memristor applications due to the direct top-down fabrication, low-temperature and potential for large scale production. A summary of the figure-of-merit of the flexible memristors is listed in Table S3 (Section S3, Supporting Information). Moreover, using pulse voltage characterization, an ultra-low energy consumption per transition of 4.5 fJ is extracted at a fixed pulse width of 600 ns, which is defined as the switching time of the printed MoS$_2$ memristor.$^{[18,19]}$ (Section S4, Supporting Information). The switching voltage (0.18 V), switching ratio (10$^7$), and the power consumption completely meet the International Technology Roadmap for Semiconductors (ITRS) for non-volatile memristor in 2030.$^{[44,45]}$

2.4. Resistive Switching Mechanism

To understand the switching mechanism, conductive atomic force microscope (c-AFM) is used to study the localized switching of the printed MoS$_2$ layer. The set-up is schematically shown in Figure 4a. A vertical nano-sized ($\approx$ 50 nm$^2$) Pt/printed MoS$_2$/Ag structure is constructed as defined by the size of Pt c-AFM tip.$^{[46]}$ First, the bottom Ag electrode is grounded, and a ramped voltage stress of 50 mV is applied to the Pt tip across the surface of the printed MoS$_2$ with one printing pass (Section S5, Supporting Information). The 3D current map plot in Figure 4a confirms the formation of conductive filaments (CFs) with different shapes and sizes.$^{[7]}$ With the positive voltage, the S vacancies are expected to play a key role in CFs formation. Subsequently, we performed 50 sequences of $I$–$V$ sweeps on the locations where current filaments are detected and found that all of them exhibit volatile RS behavior at a CC of 1 $\mu$A (Figure 4b), this is consistent with the electrical behavior as shown in Figure 3a. As guided by the arrows, the memristor switches to a LRS (arrow 1) with the applied voltage and subsequently recovers to the HRS (arrow 2) when the voltage is removed. Besides, it is found that faster switching can occur at locations with higher current density (Location 1) while no switching is observed at locations without any current filament (Location 4), as shown in Figure 4c. This confirms that the CFs are easily formed in more defective locations within the MoS$_2$ switching medium. However, in the practical printed MoS$_2$ structure with multiple printing passes, the Ag is found

Figure 3. Resistive switching characteristics. a) Volatile RS with different CC of 100 nA, 1 $\mu$A, and 10 $\mu$A. b) $I$–$V$ sweeps of a single device with 100 cycles. An ultra-low set voltage of 0.18–0.3 V is achieved. c) Non-volatile RS with CC of 100 $\mu$A, 200 $\mu$A, and 1 mA. d) Data retention at LRS measured by $I$–$t$ sampling, collected immediately after setting the device to LRS. The $V_{\text{read}}$ is 10 mV. e) Multiple states of the resistance recorded with varied CC. The read voltage is 10 mV. f) Benchmarking of the fully printed MoS$_2$ memristor with state-of-the-art memristors fabricated on flexible substrate. It achieves the lowest switching voltage with highest switching ratio.
to play a part in the successful RS. It is found that if the Ag electrode is replaced by printed carbon nanotube (CNT), the RS disappears. Instead, the \(I-V\) curve of CNT/MoS\(_2\)/CNT shows the characteristics of a surface-illuminated photo-detector \(\cite{22}\) (Section S6, Supporting Information). Considering these two conditions, the switching is most plausibly induced by metal penetration from an active electrode with \(S\) vacancies exchange.

To further investigate the switching mechanism and the kinetics of Ag filament formation in the printed MoS\(_2\), a 2D kinetic Monte Carlo (kMC) model is established in Figure 4d. Based on the experimental results, the MoS\(_2\) memristor switching medium layer is likely composed of vertically stacked MoS\(_2\) flakes to form a staggered close-packed structure. In this model, clearly gaps are present between the vertically stacked MoS\(_2\) flakes (bottom-top gap) and the laterally close-packed MoS\(_2\) flakes (side gap). There are two possible horizontal diffusion paths (\(P_{L1}\) and \(P_{L2}\)) and two possible vertical diffusion paths (\(P_{H1}\) and \(P_{H2}\)) for Ag metallic ions to form Ag filaments. \(P_{L1}\) and \(P_{L2}\) are the horizontal diffusion of Ag metallic ions through the bottom-top gaps and through the interlayer space of multilayer MoS\(_2\) flake, respectively. \(P_{H1}\) and \(P_{H2}\) are the vertical diffusion of Ag metallic ions through the side gaps and through the Mo- and S-member ring within an internal layer of a MoS\(_2\) flake, respectively. Considering all the possible diffusion paths of Ag ions in MoS\(_2\), we perform density functional theory (DFT) calculations to assess the viability of these paths. Metallic Ag ions are expected to move in the paths with the lowest horizontal and vertical diffusion barriers \(\cite{47}\).

According to the DFT calculation results, in the horizontal direction, Ag prefers to replace \(S\) to form an Ag substitutional defect (AgS) and Ag diffusion is mediated by \(S\) atoms. The calculated energy barriers of the Ag diffusion steps for \(L_{11}\), \(L_{12}\), and \(L_{22}\) in Figure 4e are similar, at \(\approx 0.29\) eV. However, the energy barrier for Ag ion to diffuse into the MoS\(_2\) interlayer is as high as 1.3 eV (\(L_{21}\) in Figure 4e), which essentially rules out the horizontal diffusion of Ag ions in the interlayer space of multilayer MoS\(_2\) flake. Once Ag ion diffuses to a nearby edge (\(H_{11}\)), it will move to the side gap spontaneously since there is no energy barrier associated with \(H_{11}\). The energy barrier for Ag ions to diffuse into the side gap (\(H_{12}/H_{11}\) in Figure 4e) along the vertical direction is 0.31 eV. It should be noted that the energy barrier for Ag ion to diffuse through the Mo- and S-member rings within perfect internal layer of a MoS\(_2\) flake is as high as 8.41 eV and that with one double \(S\) vacancy is only slightly reduced to 5.78 eV. Therefore, the vertical diffusion of Ag metallic ions through an internal layer
of a MoS2 flake (H2 in Figure 4e) is not possible. Hence, the most energetically favorable paths for Ag ions to form Ag filaments are the horizontal diffusion through the bottom-top gaps and vertical diffusion through the side gaps. Based on these paths, we perform kMC simulations with the energy barriers from our DFT calculations. Initially, no Ag ions are present in the printed MoS2 RRAM switch medium. Subsequently, the Ag ions start to diffuse along both horizontal and vertical directions under the pulse voltage. After 1 ms or 3 ms (the insets in Figure 4f), the first Ag filament is formed when the thickness is 300 or 500 nm. Thereafter, more Ag filaments are formed with increasing simulation time. This may explain the volatile-to-non-volatile transition of the resistive switching behavior of printed MoS2 memristor. More details of the kMC model and DFT simulation are described in Section S7, Supporting Information. Existing works on MoS2 switching either relies on migration of oxygen and sulfur ions at the metal/MoS2−xO interface,[17] utilizing the metallic phase,[48] or the percolation of vacancies through the defects in CVD grown MoS2.[13] However, the reported switching voltage is considerably higher due to the morphology and defect concentration of the materials. The excess sulfur vacancies induced by UALPE and the unique printed staggered close-packed structure introduce a lot of diffusion pathways for metal ion penetration to form the conductive filaments. Hence, leading to an ultra-low switching voltage and consequently, extremely low power consumption.

2.5. Dynamic Response of Artificial Synapse

The printed memristor closely mimics the structure and function of a biological synapse, as shown in Figure 5a, which holds potential to enable neuromorphic computing through

![Figure 5. Dynamic response of the fully printed MoS2 memristive artificial synapse. a) Schematic of a biological synaptic junction and the signal transmission between the pre- and post-synaptic neurons. b) Sequence of pulse train showing synapse potentiation. 50 µs pulse width and 100 µs pulse period were used. Voltage up (V_{up}, set voltage) is 0.8 V, and voltage down (V_{down}, read voltage) is 0.1 V. c) Relaxation characteristics with a short pulse width of 100 µs and a pulse period of 900 µs showing short-term plasticity (STP). Voltage up (V_{up}, set voltage) is 1 V, and voltage down (V_{down}, read voltage) is 0.1 V. d) Pulse train with a longer pulse width of 0.5 ms and a larger V_{up} = 1.2 V, showing progressive long-term potentiation (LTP). The read current increases from ≈90 µA to 200 µA after 200 pulse train and goes into a stable non-volatile regime. e) Pulse train with V_{up} = −1.2 V, and V_{down} = 0.1 V, showing a progressive long term depression (LTD). f) Post-synaptic resistance versus pulse number at different pulse amplitudes. g) Synaptic weight change versus pulse intervals, which indicates the weight update ability of the printed MoS2 synapse. The synaptic weight change is the extracted post-synaptic current change corresponding to two successive pulses. The dashed line is an exponential fit with a time constant of 1.35 ms.](image-url)
synaptic plasticity.[7,9,10,31] To study the dynamic response of printed MoS2 memristor under real working conditions, sequences of pulse train with different amplitude and frequency are applied through the top electrode while simultaneously monitoring the current. In general, the influx processes of pre-synaptic Ca2+ induce two types of synaptic plasticity, namely short-term and long-term plasticity[30,31] that are involved in learning and memory. As shown in Figure 5b, the plasticity of the artificial synapse can be measured by the post-synaptic current (read current \( I_{\text{read}} \)), which exhibits a progressive increase with the application of a consecutive high-frequency (10 kHz) pre-synaptic pulse train to the memristor. This potentiation process is expected to be related to the growth and clustering of Ag metal ions. At \( t = 3 \text{ ms} \), the sudden current increase implies the initial formation of conductive path, which is in the same time scale predicted by the kMC model. When a short pulse width of 100 \( \mu \text{s} \) is applied, the synapse shows a temporal potentiation and subsequently relaxes due to its volatile nature (Figure 5c). This behavior is analogous to the short-term plasticity in the form of PPF when the biological synapse is subject to a brief or less intense stimulation.[10] The decay during \( \tau_{\text{down}} \) is fitted with an exponential equation,[49] showing a relaxation constant of \( \approx 50 \mu \text{s} \). On the other hand, long-term potentiation (LTP) and depression (LTD) are the two forms of activity-dependent long-term synaptic plasticity that last minutes or longer.[9,31] This can be triggered with an intense pulse train to set the synapse into the non-volatile regime. As shown in Figure 5d, the current exhibits a long-lasting increase under positive pulse stimuli with a voltage up \( (V_{\text{up}}) \) of 1.2 V, voltage down \( (V_{\text{down}}) \) of 0.1 V, and a frequency of 1 kHz. Since the \( I_{\text{read}} \) goes into non-volatile regime, the current is observed to stay constant when the first pulse train is ended. When the second pulse train is initiated, the current will increase again. This behavior is analogous to LTP. To disrupt the Ag filament and thus relax the synapse, a pulse train with an opposite polarity \( (V_{\text{up}}) \) is –1.2 V, \( V_{\text{down}} \) is 0.1 V, and frequency is 1 kHz is needed to decrease the current, resembling the LTD (Figure 5e). In addition, Figure 5f shows that multiple resistance states can be easily programmed by varying the pulse amplitude, which is found to improve the adaptive learning ability of neuromorphic computing.[10] Moreover, Figure 5g depicts that the synaptic weight change can be modulated by the frequency of the applied pulses, where a short time interval between two successive pulses results in a larger potentiation in the memristor weight change. This implies the successful implementation of spike-rate-dependent plasticity (SRDP) characteristic,[9] a critical aspect of biological models for neuromorphic computing. The fitted constant of 1.35 ms is comparable to the response time of biological synapses.[30,31]

3. Conclusion

In conclusion, we have fabricated fully printed, mechanically flexible Ag/MoS2/Ag memristors by aerosol-jet printing which demonstrates a robust resistive switching with both volatile and non-volatile memory functions that allow a wide tuneability of discrete resistance states and switching ratios at an ultra-low switching voltage. In addition, the printed MoS2 memristors allow a successful emulation of both short- and long-term synaptic plasticity functions including PPF, relaxation, LTP/LTD, and SRDP within a single device, which are promising to enable complex neuromorphic learning. The development of a functional 2D ink platform and a scalable, low temperature printing technology may offer potential to realize large-scale 3D heterogeneous integration on polymeric substrates, which is envisaged to provide a platform to advance printable electronics toward “system-on-plastics.”

4. Experimental Section

Aerosol-Jet Printer: The entire memristor element was printed using an aerosol-jet printer (Optomec) on Kapton substrates (100 \( \mu \text{m} \)) at room temperature. Ag (Clariant) lines were first deposited with a nozzle diameter of 300 \( \mu \text{m} \). The width of the printed lines was \( \approx 100 \mu \text{m} \), achieved by controlling both the sheath and carrier gas flow. The Ag lines were then transformed into conductive lines via a localized laser sintering process. The MoS2 switching layer was printed using the ultrasonic mode, where the ink was first sonicated to atomize the ink before being carried out by the \( N_2 \) carrier gas. To ensure there was no shorting between the top and bottom Ag electrodes, the MoS2 medium layer was printed using 10 printing passes.

Electrical Measurement: \( I-V \) sweeps were measured in ambient condition at room temperature using an Agilent 4155B semiconductor parameter analyzer and a probe station. The pulse measurements were carried out using a Cascade probe station connecting to a 4200-SCS Keithley semiconductor analyzer. The applied pulse amplitude is larger than the set voltage in \( I-V \) measurement probably due to the parasitic resistance of the equipment.

XPS: Core level spectra were collected using a monochromatic Al K\( \alpha \) X-ray source (\( h\nu = 1486.69 \text{ eV} \)). The binding energies of all spectra were referenced to C 1s which was set to 285 eV. Then Gaussian and Lorentzian line shapes with a Shirley background subtraction were used to fit the raw data. For the S 2p core level spectra, the well-resolved two peaks corresponding to S 2p1/2 (162.67 eV) and S 2p3/2 (161.53 eV) were extracted. For the Mo 3d core level spectra, two core level peaks at 228.74 and 231.91 eV could be attributed to Mo 3d5 and Mo 3d3. Besides, a small amount (3.28%) of molybdenum oxide was also observed at 235.22 eV. The atomic ratio of S/Mo was calculated as the atomic concentration of S over Mo and Mo oxides.

Supporting Information

Supporting Information is available from the Wiley Online Library or from the author.

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Conflict of Interest

The authors declare no conflict of interest.
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